

IN THE CLAIMS

This listing of claims will replace all prior versions, listings, of claims in the application.

1. (Currently Amended) A method comprising:

receiving a read request relating to a first line of data in a coherent memory system;

receiving a write request relating to the first line of data at about the same time as the read request is received;

detecting that the read request and the write request both relate to the first line;

determining which request of the read and write requests should proceed first; ~~and~~

delaying the request which is not the request which should proceed first; and

completing the request of the read and write request which should proceed first,

wherein if the write request is received first, then the write request is determined to be the request which should proceed first, and further comprising satisfying the read request with a new value if the write request is received before the request, the new value coming from the write request.

2. (Currently Amended) The method of claim 1 wherein:

if the read request is received first, then the read request is determined to be the request which should proceed first and the write request is delayed.

3. (Canceled)

4. (Canceled)

5. (Canceled)

6. (Canceled)

7. (Original) The method of claim 1 wherein:

detecting that the read request and the write request both relate to the first line is accomplished by comparing an address of the read request and an address of the write request.

8. (Original) The method of claim 7 wherein:

the method is performed within circuitry of a component, the component suitable for coupling to a first processor and to a second processor, the read request originating from the first processor and the write request originating from the second processor.

9. (Original) The method of claim 8 wherein:

the read request is received first, the read request is determined to be the request which should proceed first, the write request is delayed.

10. (Currently Amended) A method comprising:

receiving a read request relating to a first line of data in a coherent memory system;
receiving a write request relating to the first line of data at about the same time as the
read request is received;

detecting that the read request and the write request both relate to the first line by
comparing an address of the read request and an address of the write request;

determining which request of the read and write request should proceed first;

completing the request of the read and write request which should proceed first. The
~~method of claim 8 wherein:~~ wherein if the write request is received first, then the write request is
determined to be the request which should proceed first, and the read request is delayed; and the
method further comprises ~~comprising:~~ satisfying the read request with a new value, the new
value coming from the write request,

wherein the method is performed within circuitry of a component, the component suitable
for coupling to a first processor and to a second processor, the read request originating from the
first processor and the write request originating from the second processor.

11. (Canceled)

12. (Canceled)

13. (Canceled)

14. (Canceled)

15. (Currently Amended) An apparatus comprising:

means for receiving a read request relating to a first line of data in a coherent memory
system;

means for receiving a write request relating to the first line of data at about the same time
as the read request is received, the means for receiving a write request coupled to the means for
receiving the read request;

means for detecting that the read request and the write request both relate to the first line, the means for detecting coupled to the means for receiving the write request and the means for receiving the read request;

means for determining which request of the read and write request should proceed first, the means for determining coupled to the means for detecting;

means for delaying the request which is not the request which should proceed first;

first means for completing the request of the read and write request which should proceed first, the first means coupled to the means for determining, the means for receiving the read request and the means for receiving the write request; and

second means for completing the request of the read and write request which was not determined to be the request which should proceed first, the second means coupled to the means for determining, the means for receiving the read request and the means for receiving the write request, the second means further for using a value of the write request to satisfy the read request when the means for delaying delays the read request.

16-22 (Canceled)

23. (Original) The method of claim 1 further comprising:
completing the request of the read and write request which was not determined to be the request which should proceed first.

24. (Currently Amended) A method comprising:
receiving a read request relating to a first line of data in a coherent memory system;

receiving a write request relating to the first line of data at about the same time as the read request is received;

detecting that the read request and the write request both relate to the first line;

determining which request of the read and write request should proceed first;

completing the request of the read and write request which should proceed first;

~~The method of claim 23 wherein:~~ if the read request is received first, then the read request is determined to be the request which should proceed first; and

delaying the request which is not the request which should proceed first; and wherein:

completing the request includes the read request completing the write request independent of an origination node of the write request.